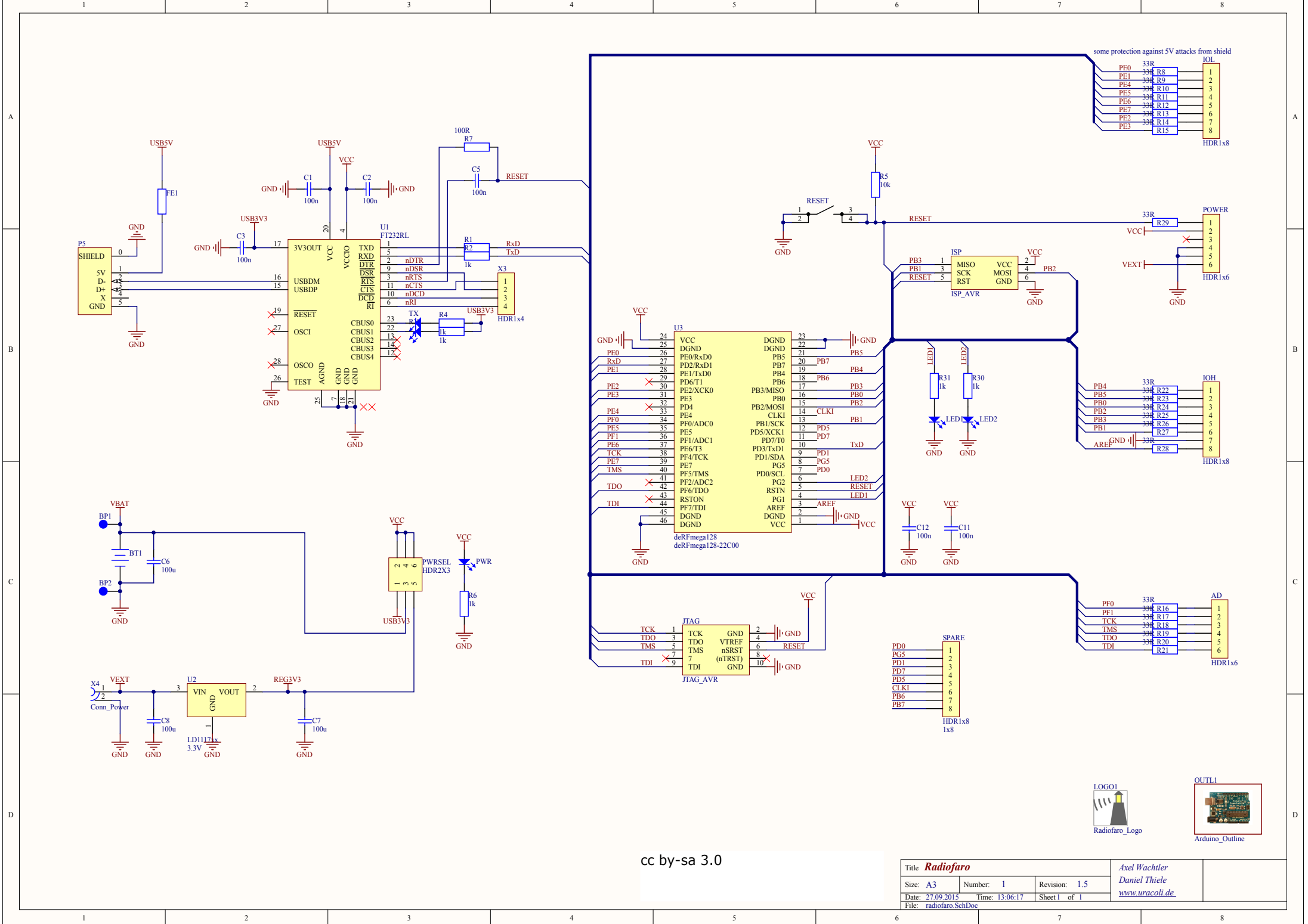




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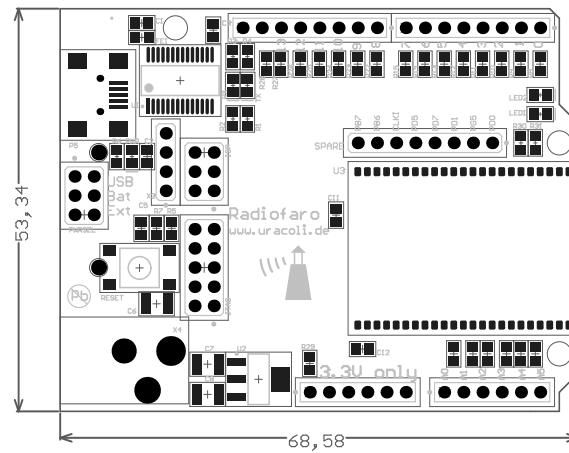
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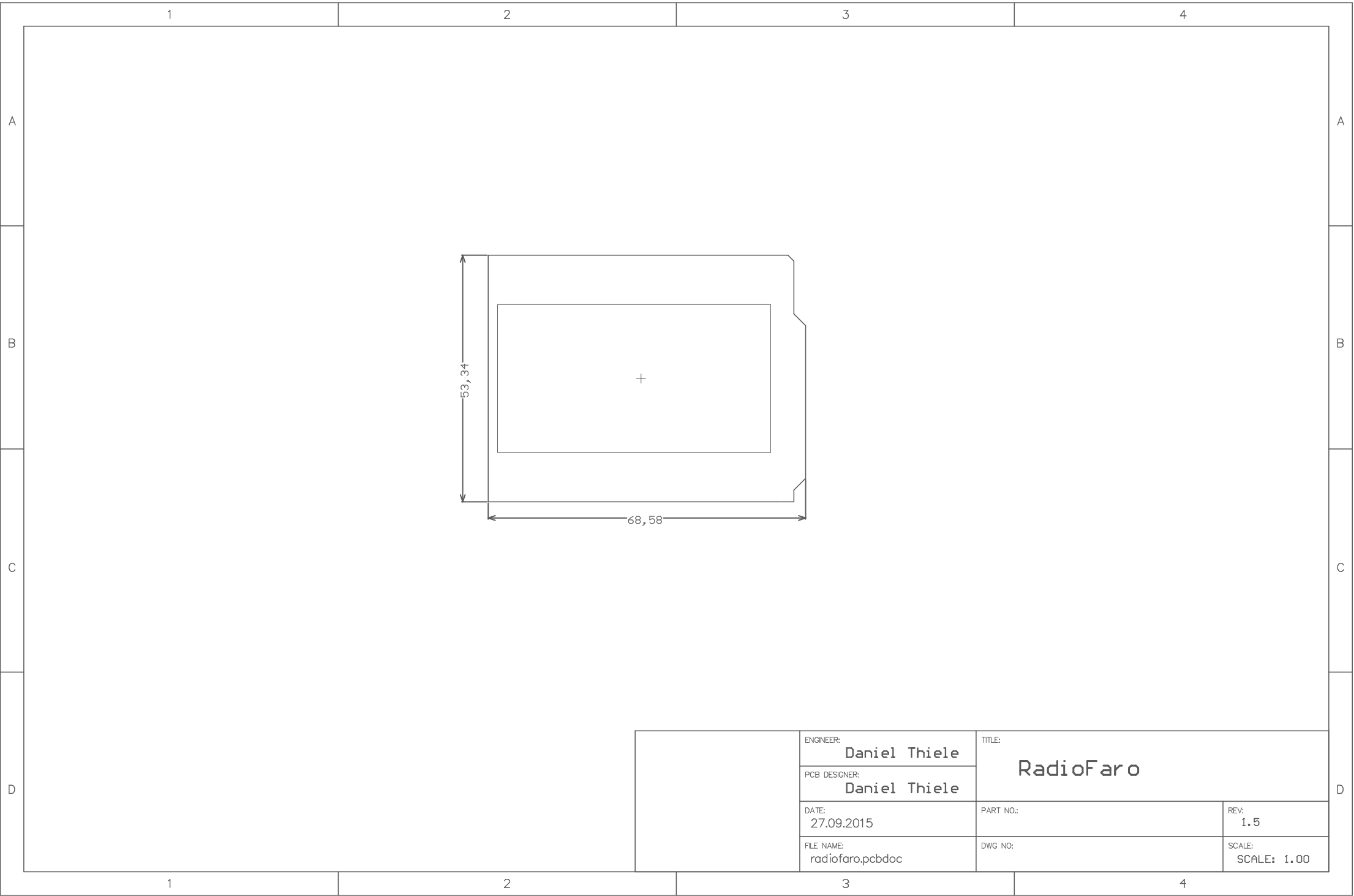
|  |                                       |                            |                       |
|--|---------------------------------------|----------------------------|-----------------------|
|  | ENGINEER:<br><b>Daniel Thiele</b>     | TITLE:<br><b>RadioFaro</b> |                       |
|  | PCB DESIGNER:<br><b>Daniel Thiele</b> |                            |                       |
|  | DATE:<br>27.09.2015                   | PART NO.:                  | REV:<br>1.5           |
|  | FILE NAME:<br>radiofaro.pcbdoc        | DWG NO.:                   | SCALE:<br>SCALE: 1.00 |

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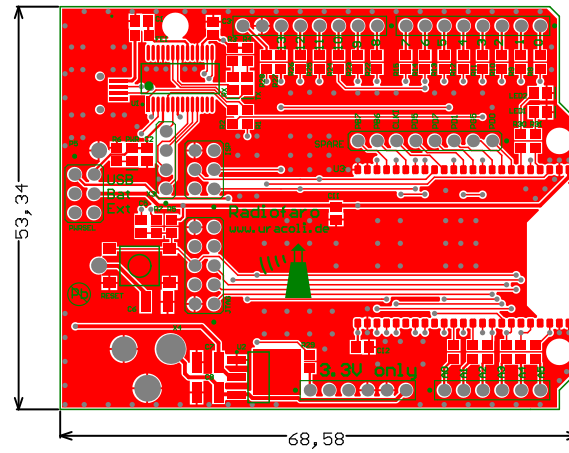
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|  |                                       |                            |                       |
|--|---------------------------------------|----------------------------|-----------------------|
|  | ENGINEER:<br><b>Daniel Thiele</b>     | TITLE:<br><b>RadioFaro</b> |                       |
|  | PCB DESIGNER:<br><b>Daniel Thiele</b> |                            |                       |
|  | DATE:<br>27.09.2015                   | PART NO.:                  | REV:<br>1.5           |
|  | FILE NAME:<br>radiofaro.pcbdoc        | DWG NO.:                   | SCALE:<br>SCALE: 1.00 |

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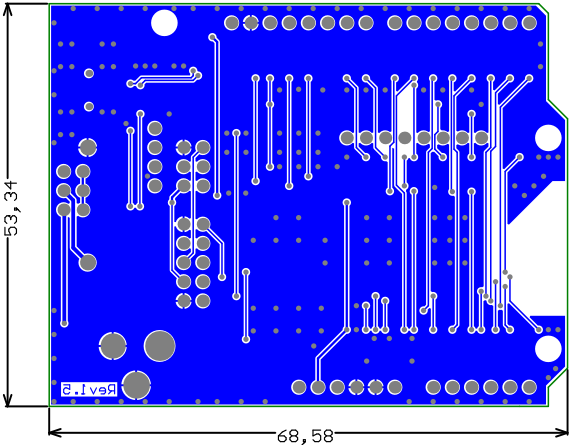
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|  |                                |  |                         |                       |
|--|--------------------------------|--|-------------------------|-----------------------|
|  | ENGINEER:<br>Daniel Thiele     |  | TITLE:<br><br>RadioFaro |                       |
|  | PCB DESIGNER:<br>Daniel Thiele |  |                         |                       |
|  | DATE:<br>27.09.2015            |  | PART NO.:               | REV:<br>1.5           |
|  | FILE NAME:<br>radiofaro.pcbdoc |  | DWG NO.:                | SCALE:<br>SCALE: 1.00 |

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